TECHNICAL MANUAL

for

TONE INTELLIGENCE UNIT MODEL TIS-3

(TH-39A/UGT)



THE TECHNICAL MATERIEL CORPORATION

MAMARONECK, N.Y. OTTAWA, CANADA

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Issue Date: 15 AUGUST 1963

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THE TECHNICAL MATERIEL CORPORATION

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- 1. That any claim of defect under this warranty is made within sixty (60) days after discovery thereof and that inspection by TMC, if required, indicates the validity of such claim to TMC's satisfaction.
- 2. That the defect is not the result of damage incurred in shipment from or to the factory.
- 3. That the equipment has not been altered in any way either as to design or use whether by replacement parts not supplied or approved by TMC, or otherwise.
- 4. That any equipment or accessories furnished but not manufactured by TMC, or not of TMC design shall be subject only to such adjustments as TMC may obtain from the supplier thereof.

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No warranties, express or implied, other than those specifically set forth herein shall be applicable to any equipment manufactured or furnished by TMC and the foregoing warranty shall constitute the Buyers sole right and remedy. In no event does TMC assume any liability for consequential damages, or for loss, damage or expense directly or indirectly arising from the use of TMC Products, or any inability to use them either separately or in combination with other equipment or materials or from any other cause.

*Electron tubes also includ semi-conductor devices.

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- 2. Serial Number of Equipment.
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- 4. Nature of defect or cause of failure.
- 5. The contract or purchase order under which equipment was delivered.

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- 2. TMC Part Number.
- 3. Equipment in which used by TMC or Military Model Number.
- 4. Brief Description of the Item.
- 5. The Crystal Frequency if the order includes crystals.

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TMC's Warranty specifically excludes damage incurred in shipment to or from the factory. In the event equipment is received in damaged condition, the carrier should be notified immediately. Claims for such damage should be filed with the carrier involved and not with TMC.

All correspondence pertaining to Warranty Claims, return, repair, or replacement and all material or equipment returned for repair or replacement, within Warranty or otherwise, should be addressed as follows:

THE TECHNICAL MATERIEL CORPORATION

Engineering Services Department 700 Fenimore Road Mamaroneck, New York

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TABLE OF CONTENTS

Paragrap	ph	Page	Paragraph	Page
SI	ECTION 1 — GENERAL INFORMATION	İ	SECTION 4 — PRINCIPLES OF OPE (Cont'd)	RATION
1-1 1-2 1-3 1-4	General	1-1 1-1	4-3 Second Keyer and Test Switch . 4-4 Crystal Oscillator 4-5 Reactance Controlled Oscillator 4-6 CW Oscillator and Mixer	· · 4-2 · · 4-2
S	ECTION 2 — INSTALLATION		4-7 FAX Isolator and Audio Amplifier 4-8 Power Supply	
2-1 2-2	Unpacking and Visual Inspection		SECTION 5 — TROUBLESHOOTING	
2-3 2-4	Installation	2-1	5-1 General	5-1
_			SECTION 6 - MAINTENANCE	
5	ECTION 3 — OPERATOR'S SECTION			
3-1 3-2 3-3	General	3-1	6-1 General	6-0
			SECTION 7 — PARTS LIST	
S	ECTION 4 — PRINCIPLES OF OPERAT	ION		
			7-1 General	7-0
4-1 4-2	Overall Block Diagram Analysis Input Circuit and First Keyer		SECTION 8 — SCHEMATIC DIAGRA	MS

LIST OF ILLUSTRATIONS

Figure	e Page	Figure		Page
	SECTION 1 — GENERAL INFORMATION		SECTION 4 — PRINCIPLES OF OPERATIO	ON
1-1	Tone Intelligence Unit TIS-3, Front Angle View	4-8	Power Supply, Simplified Schematic	
	SECTION 2 — INSTALLATION		Diagram	4-12
		•	SECTION 5 — TROUBLESHOOTING	
2-1 2-2	Power Supply, Wiring Diagram 2-1 Oven Wiring Diagram 2-1	•	ACTION 5 - ROODLISHOOMING	
2-3	Tone Intelligence Unit TIS-3,	5-1	Voltage and Resistance Measure-	
	Installation Drawing 2-2	5-2	ments	5-3 5-4
	SECTION 3 — OPERATOR'S SECTION	5-3	Top View of Chassis, Parts	5-4
	Stellon 0 — Of ERATOR 3 Stellon	5-4		5-4
3-1	Front Panel Operating Controls 3-1	5-4	Bottom View of Chassis, Parts Location	5-5
		5-5	Right View of Chassis, Parts	0-0
	SECTION 4 — PRINCIPLES OF OPERATION	5-6	Rear View of Chassis, Parts	5-6
4-1	Tone Intelligence Unit TIS-3, Overall	5-7	Location	5-6
4-2	Block Diagram		Location	5-7
4-3	Diagram 4-5 Second Keyer, Simplified Schematic		SECTION 6 - MAINTENANCE	
1-0	Diagram 4-6	3	ECTION 6 - MAINTENANCE	
4-4	Crystal Oscillator, Simplified	6-1	Test Setup No. 1	۰.
4 5	Schematic Diagram 4-7	6-2		6-5 6-5
4-5	200 Kc Oscillator and Reactance Tube, Simplified Schematic	6-3	Test Setup No. 3	6-5
	Diagram 4-8			
4-6	CW Oscillator and Mixer, Simplified	S	SECTION 8 - SCHEMATIC DIAGRAMS	
4-7	Schematic Diagram 4-9 FAX Isolator, Cathode Follower and			
•	Audio Amplifier, Simplified	8-1	Tone Intelligence Unit TIS-3,	
	Schematic Diagram 4-11		Detailed Schematic Diagram 8-3/	8-4

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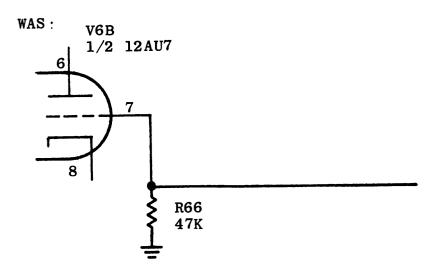


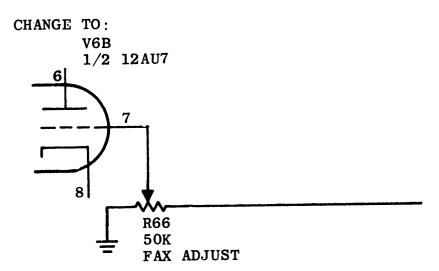
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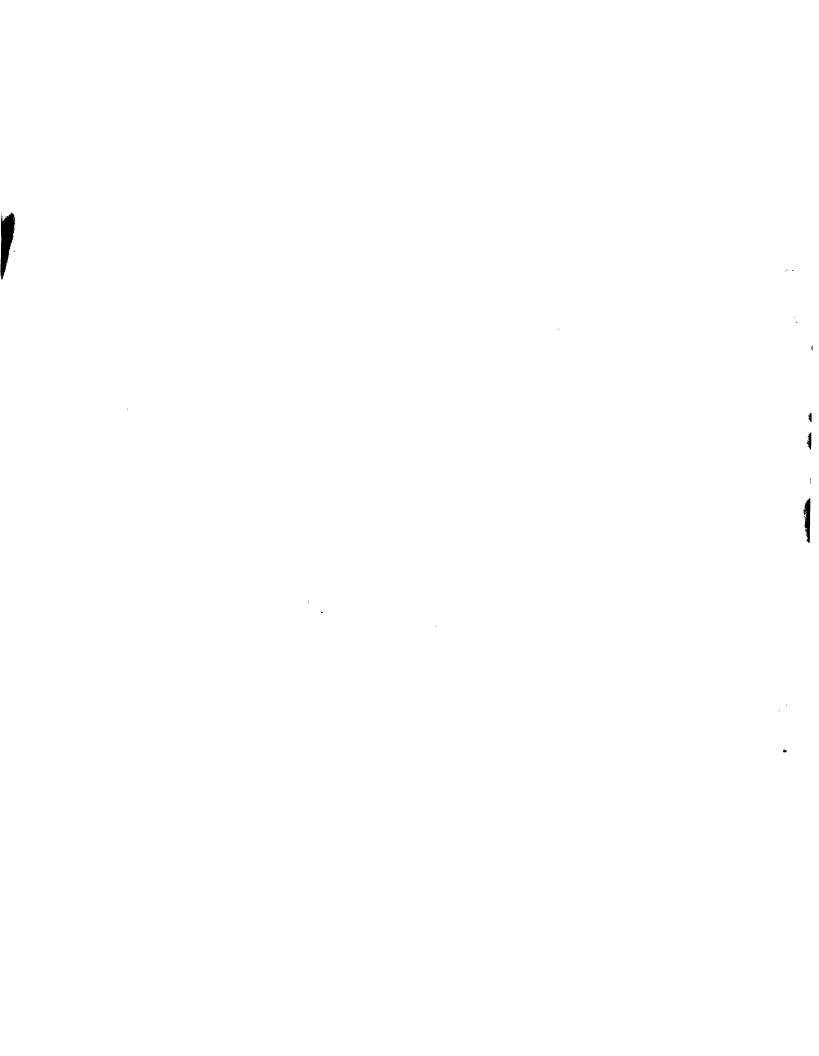
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Page 4-7 Figure 4-7









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					Date _	12/9/	63	
Manual affected:	Tone	Intelligence	Unit	Model	TIS-3	IN	-2025/230	

Page 8-3-8-4 Figure 8-1

R68 (15K) is shown located within the oven.

Relocate R68 on the schematic so that it appears outside the oven.

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			~s,
			•
			1
			ı.
			•

LIST OF TABLES

Table		Page	Table	Pag
	SECTION 1 — GENERAL INFORMATION		SECTION 5 — TROUBLESHO	OTING
1-1 1-2	Technical Characteristics Tube and Semiconductor Complement		5-1 Troubleshooting Procedure	e 5-1
;	SECTION 3 — OPERATOR'S SECTION		SECTION 6 - MAINTENAN	CE
3-1	Operating Controls	3-2	6-1 Alignment Procedure	6.1



Figure 1-1. Tone Intelligence Unit TIS-3, Front Angle View

SECTION 1 GENERAL INFORMATION

1-1. **GENERAL**. (See figure 1-1.)

Tone Intelligence Unit TIS-3 (commonly called TIS) is an audio-shift keyer designed to be operated in conjunction with a synthesized exciter. Its purpose is to generate a keyed or frequency-shifted audio tone output, so that the carrier generated by the exciter will not have to be keyed or shifted directly. Therefore, the frequency stability of the carrier is maintained.

One of the unique features of the TIS is that it does not use frequency multipliers to develop the frequency shift. A reactance-controlled oscillator is used to generate a center frequency, that is shifted in direct proportion with the applied grid voltage. In the FSK mode, the grid voltage is controlled by the SHIFT CPS control on the front panel, and the amount of shift is read directly on a counter-type knob. In the FAX mode, the grid voltage is regulated by the level of the input signal and the SHIFT CPS control is bypassed. The audio tone output is developed by mixing the frequency-shifted output with a reference frequency generated by a second (crystal) oscillator. The difference frequency is amplified and applied directly to the exciter.

The TIS is capable of generating four discrete audio outputs (1000 cps, 1900 cps, 2000 cps and 2550 cps) that conform to all the CCIR specifications for telegraphy. In addition, provisions have been made so that the user can insert a crystal of his own choosing to develop a specific audio tone.

The 1000 cycle output is used in the CW mode only in conjunction with an external handkey. The 1900 cycle tone is used in the FAX mode and is the international standard. The 2000 cycle tone is used in the FSK mode and is compatible with transmitters and receivers limited to tuning in 1 kc steps. The 2250 cycle tone is used in the FSK mode and is a widely used standard for 850 cps FSK.

1-2. FUNCTIONAL DESCRIPTION.

The TIS functionally consists of three oscillators (reactance-controlled, crystal and sidetone), a mixer and a push-pull audio amplifer.

The reactance-controlled oscillator generates a 200 kc output that is free from transients and shifted (in the FSK and FAX modes) indirect proportion with the applied grid voltage. The frequency shifted output is mixed with a reference frequency, generated by the crystal oscillator and selected to give the correct audio tone (difference) output. The difference frequency (shifted audio tone) is then applied by the pushpull amplifier to the exciter. In the FSK and FAX modes, B+ is not applied to the sidetone (CW) oscillator.

In the CW mode, B+ is removed from the reactance controlled oscillator and applied to the sidetone oscillator. The cathode voltage of the sidetone oscillator is interrupted by the external handkey, and its output is a keyed 1000 cycle tone. This keyed 1000 cycle tone is then applied to the exciter by the pushpull amplifier.

1-3. PHYSICAL DESCRIPTION.

Tone Intelligence Unit TIS-3 (see figure 1-1) is mounted on a panel 19 inches wide and 5-1/4 inches high, for installation in a standard relay rack, and weighs approximately 54 lbs. when packed for shipment. The front panel is 3/16 of an inch thick and is finished in gray enamel.

The frame extends 14-5/16 inches behind the front panel, is self-supporting when the proper hardware is used, and contains a main chassis and a power supply chassis. The main chassis is mounted horizontally, and the power supply chassis is mounted vertically on the right side of the frame facing the front panel.

All operating controls, fuses, and indicators are mounted on the front panel, and all tubes and adjustments are accessible from the top of the main chassis. The inputs and outputs are connected to a terminal block mounted on the rear of the main chassis and the power jack is mounted at the rear of the power supply chassis.

1-4. QUICK REFERENCE DATA.

Table 1-1 lists the major electrical characteristics of the TIS. Table 1-2 lists the tubes and semiconductors used in the TIS and their function.

TABLE 1-1. TECHNICAL CHARACTERISTICS.

CW tone frequency:

1000 cps.

FAX input:

0 to +10 volts for a linear shift of 800 cps.

FSK output frequency shift:

12 to 1000 cps, continuously adjustable.

Frequency stability:

Better than 0.5% for: $0-50^{\circ}$ C ambient temperature change, +10% line voltage variation, 0 to 95% relative humidity.

Fuse protection:

1 amper? (115 volts). 0,5 ampere (230 volts).

Input compensating impedance to

keying line:

KEY MODE control at 50V - 47,000 ohms KEY MODE control at 100V - 100,000 ohms KEY MODE control at 20MA - 2,200 ohms KEY MODE control at 60MA - 1,000 ohms

Keying input:

50v, 100v, 20ma, 60ma (all neutral, float-

ing, or either side grounded,.

Keying speed:

Up to 150 bauds (200 wpm) for FSK; up to 150 bauds for CW; and up to 400 bauds for

FAX.

Output audio center frequencies:

1900 cps, 2000 cps, 2550 cps, spare.

Output impedance:

600 ohms, balanced.

Output level:

Continuously adjustable to 0 dbm (0 to 1

milliwatt)

Oven temperature:

 $70^{\circ} + 2^{\circ}C.$

Power input:

105/115/125/210/230 volts; 50-60 cps; single phase, 100 watts continuous, 170

watts intermittent.

TABLE 1-2. TUBE AND SEMICONDUCTOR COMPLEMENT.

REFERENCE DESIGNATION	TYPE	FUNCTION
V1	6J6	200-kc oscillator
V2	5963	Reactance tube
V3	12AU7	2nd keyer
V4	12AU7	1st keyer
V 5	12AU7	Mixer
V 6	12AU7	CW oscillator/FAX isolator
V7	12AU7	Audio amplifier
V8	6AG5	Crystal oscillator
V9	OA2	Negative bias voltage regulator
V10	OA2	B+ voltage regulator
CR1, CR2	IN67	Gates
CR3 through CR6	IN547	Rectifier
CR7, CR8	IN67	Negative clipper
CR9	DD100	Full wave rectifier
1.9		

SECTION 2 INSTALLATION

2-1. UNPACKING AND VISUAL INSPECTION.

The TIS has been tested and calibrated prior to shipment. Therefore, only minor adjustments are required before operating the unit.

Unpack the TIS carefully and inspect the packing material for parts which may have been shipped as loose items. Immediately after unpacking the TIS, perform the following visual inspection:

- (1) Inspect front panel and exterior of case for dents, scratches, cracked meter faces, and broken or missing controls.
- (2) Make certain fuses are installed in holders on front panel.
- (3) Remove top cover and inspect chassis for broken tubes and loose hardware. Make certain tubes are seated in sockets and replace cover.
- (4) Remove bottom cover; inspect for broken wires and loose hardware and replace cover.

Although the carrier is liable for any damage to the equipment, Technical Materiel Corporation will assist in describing and providing for repair of damaged items.

2-2. POWER REQUIREMENTS.

The power supply of the TIS has been designed for use with either 115 volt or 230 volt, 50/60 cycle, single-phase, three wire, 100 watt primary power. However, unless otherwise specified, the power supplies are wired at the factory for 115 vac, 50/60 cycle primary power, and minor wiring changes are required if 230 vac primary power is to be used.

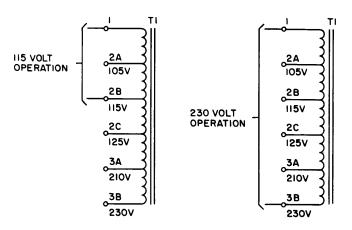


Figure 2-1. Power Supply, Wiring Diagram

Figures 2-1 and 2-2 illustrate the wiring diagrams for 115 vac and 230 vac primary power.

2-3. INSTALLATION. (See figure 2-3.)

Mount the TIS in a standard 19 inch relay rack and secure with hardware provided. Connect inputs and outputs to pins of terminal block mounted on rear of main chassis (see figure 8-1) and connect power cord from jack at rear of power supply chassis to any convenient 115 vac receptacle. Turn B+/STANDBY switch to ON.

2-4. PRE-OPERATIONAL CHECKS.

The TIS is a highly stable instrument requiring an aging period of at least 24 hours of continuous operation. The equipment is designed for continuous operation and should only be deenergized for detailed repairs. Failure to comply with these stipulations will result in degradation of the stability.

After the initial aging period has elapsed, the OVEN indicator lamps should cycle continuously at regular intervals.

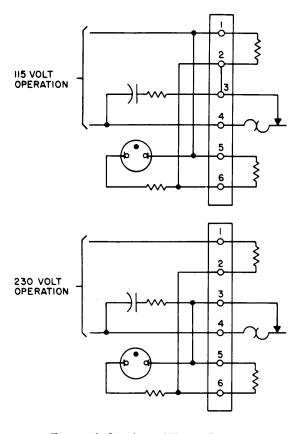


Figure 2-2. Oven Wiring Diagram

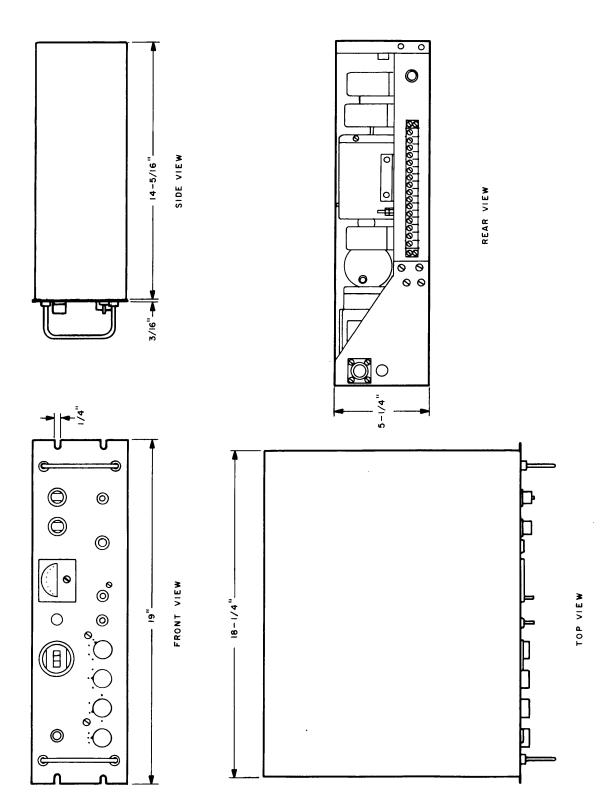


Figure 2-3. Tone Intelligence Unit TIS-3, Installation Drawing

SECTION 3 OPERATORS SECTION

3-1. GENERAL .

For optimum stability, the TIS must be continuously energized and should be shut down only in the event of a failure. Therefore, an independent source of primary power must be provided so that the TIS can remain energized when the associated equipment is shut down.

3-2. OPERATING CONTROLS.

All the operating controls, fuses, and indicators are located on the front panel. Table 3-1 lists these operating controls and their function (see figure 3-1).

3-3. OPERATING PROCEDURES.

- a. To operate the equipment in the FSK mode, proceed as follows:
- (1) Make certain OVEN indicator lamp is cycling at regular intervals.
 - (2) Turn FUNCTION switch to FSK.
 - (3) Turn TEST switch to LINE (normal position).
- (4) Turn KEY MODE switch to output level of external source providing input.

- (5) Turn CENTER FREQ CPS switch to desired audio output frequency (normally 2550 cps).
- (6) Turn EXCITER CH1 and EXCITER CH2 switches to appropriate mode.
 - (7) Apply FSK signal.
- (8) Adjust LEVEL ADJ control for desired output (drive) level.
- \underline{b} . To operate the equipment in the FAX mode, proceed as follows:
- (1) Make certain OVEN indicator lamp is cycling at regular intervals.
 - (2) Turn FUNCTION switch to FAX.
 - (3) Turn TEST switch to LINE.
- (4) Turn CENTER FREQ CPS switch to desired audio output (normally 1900 cps).
- (5) Turn EXCITER CH1 and EXCITER CH2 switches to appropriate mode.
- (6) Adjust LEVEL ADJ control for desired output (drive) level.
 - (7) Apply FAX signal.

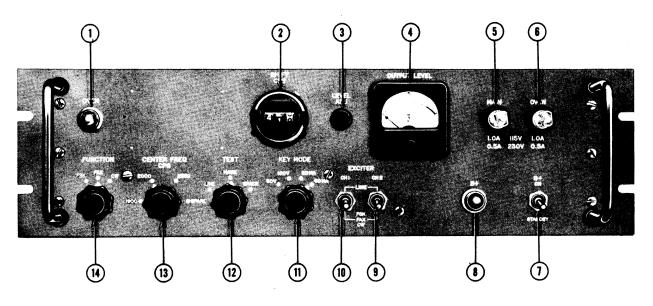


Figure 3-1. Front Panel Operating Controls

TABLE 3-1. OPERATING CONTROLS

ITEM (See Fig. 3-1)	CONTROL OR INDICATOR	FUNCTION
1	OVEN indicator lamp I2	Indicates oven containing frequency determining components of reactance controlled oscillator is operating.
2	SHIFTS CPS control R31	In FSK mode, shifts audio tone output by amount indicated on readout.
3	LEVEL ADJ control R49	Adjusts level of output (drive) applied to load.
4	OUTPUT LEVEL meter M1	Indicates level of output in dbs.
5	MAIN fuse F1	Protects equipment from current overloads.
6	OVEN fuse F2	Protects oven from current overloads.
7	B+/STANDBY switch S7	Applies B+ to all circuits.
8	B+ indicator lamp I1	Indicates B+ is applied to all stages.
9	EXCITER CH2 switch S5	Applies output to load.
10	EXCITER CH1 switch S4	Applies output to load.
11	KEY MODE switch S3	Matches input level of signal applied by external source to output level of crystal oscillator.
12	TEST switch S2	In FSK mode, provides a continuous mark or space for test purposes.
13	CENTER FREQ CPS switch S6	Selects center frequency of audio tone output.
14	FUNCTION switch S1	Selects mode of operation.

- \underline{c} . To operate the equipment in the CW mode, proceed as follows:
- (1) Make certain OVEN indicator lamp is cycling at regular intervals.
 - (2) Turn FUNCTION switch to CW.
 - (3) Turn TEST switch to LINE.

- (4) Turn KEY MODE switch to level of input signal.
- (5) Turn EXCITER CH1 and EXCITER CH2 switches to appropriate modes.
 - (6) Operate handkey.

SECTION 4 PRINCIPLES OF OPERATION

4-1. OVERALL BLOCK DIAGRAM ANALYSIS.

Figure 4-1 is an overall block diagram of the TIS. The FSK and CW inputs are applied across the selected contacts of KEY MODE switch S3 to the center taps of transformers T3 and T4 and combined with the reference frequency output of crystal oscillator V8. The combined (r-f) pulses are then applied to a full wave rectifier bridge (CR9) and applied to the grid of first keyer V4B, operating as a dc amplifier.

The output of the first keyer is applied across TEST switch S2 to the grid of second keyer V3A, also operating as a dc amplifier. During normal operation, the TEST switch is kept in the LINE position. The MARK position applies a constant voltage to the grid of the second keyer to simulate a continuous mark for test purposes, and the SPACE position grounds the grid of the second keyer to simulate a continuous space for test purposes. The amplified pulse output of the second keyer is applied to the FSK or CW contacts of FUNCTION switch S1.

The FAX input is applied across FAX ADJ control R66, FAX isolator V6B and FAX BIAS ADJ control R44 to the FAX contacts of the FUNCTION switch. The FAX ADJ control sets the threshold bias of the FAX isolator, operating as a cathode follower, and the FAX BIAS ADJ control adjusts the output level of the FAX isolator, applied to the grid of reactance control V2 across the FAX contacts of the FUNCTION switch.

In the FSK mode, B+ is removed from CW oscillator V6A by the FUNCTION switch, and the amplified output of the second keyer is applied across SHIFT CPS control R31 to the grid of the reactance control. Oscillator V1 generates a stable 200 kc frequency that is offset by an amount inversely proportional to the amplitude and polarity of the grid voltage applied to the reactance control. The offset frequency output of oscillator V1 is heterodyned in mixer V5 with the reference frequency from the cyrstal oscillator, and the difference frequency (2 kc ± frequency shift) applied to the grids of audio amplifier V7 across transformer T2.

In the FAX mode, B+ is still removed from the crystal oscillator and the adjusted output of the FAX isolator is applied across the FAX contacts of the FUNCTION switch to the grid of the reactance control. The SHIFTS CPS control is completely bypassed and the output frequency of oscillator V1 is offset by an amount inversely proportional to the amplitude and polarity of the input pulses. The offset frequency output of oscillator V1 is then mixed

with the reference frequency from the crystal oscillator, and the difference frequency applied to the grids of the audio amplifier.

In the CW mode, B+ is applied to the CW oscillator by the FUNCTION switch, and B+ is removed from oscillator V1. The audio tone output (1000 cycles) of the CW oscillator is applied to the grids of the audio amplifier across transformer T2, and at the same time, the amplified pulses from the second keyer applied to the cathode gate the pushpull output of the audio amplifier is synchronization with the input pulses from the external handkey.

Regardless of the mode selected, the push-pull output of the audio amplifier is applied across the two channel switches in parallel (when turned to FSK, FAX, CW) and monitored continuously on the OUT-PUT LEVEL meter.

4-2. INPUT CIRCUIT AND FIRST KEYER.

Figure 4-2 is a simplified schematic of KEY MODE switch S3, balanced transformers T3 and T4, bridge rectifier CR9 and first keyer V4B.

The CW or FSK input pulses are applied across pins 13 and 14 of terminal block E3, mounted on the rear of the main chassis, to the wiper of the KEY MODE switch and the center tap of transformer T3. The resistors connected to the contacts of the KEY MODE switch match the level of the input pulses with the output level of crystal oscillator V8 at the secondary of transformer T4. For a large positive input (mark) pulse, diodes CR1 and CR2 conduct and the combined signal is applied across transformer T3 to bridge rectifier CR9. For a negative input (space) pulse, diodes CR1 and CR2 do not conduct, and there is no output across transformer T3.

The 50V and 100V contacts of the KEY MODE switch are used to match the output level of a conventional handkey, and the 20MA and 60MA contacts match the output level of conventional teletypewriter equipment. The rectified pulses from the bridge rectifier are amplified and inverted by the first keyer and applied to the grid of second keyer V3A across TEST switch S2 (see figure 4-3).

4-3. SECOND KEYER AND TEST SWITCH.

Figure 4-3 is a simplified schematic of TEST switch S2, second keyer V3A and FUNCTION switch S1.

The combined (r-f) pulses from first keyer V4B are applied across the TEST switch to the grid of the second keyer. The TEST switch is normally kept in the LINE position, and the output of first keyer V4B is coupled directly to the grid of the second keyer. In the MARK position, a continuous voltage is applied to the grid of the second keyer to simulate a continuous MARK pulse for test purposes. In the SPACE position, the grid of the second keyer is grounded to simulate a continuous SPACE pulse.

The second keyer amplifies and inverts the pulses and its output is coupled by cathode follower V3B to the FUNCTION switch and cathode follower V4A.

In the FSK mode, the output of cathode follower V3B is applied across SHIFT BALANCE control R26, SHIFT CALIBRATE control R28, SHIFT CPS control R31, and the FUNCTION switch to the grid of reactance control V2. The SHIFT BALANCE control sets the zero point for the frequency shift, which may be positive or negative. The SHIFT CALIBRATE control sets the SHIFT CPS counter at zero when there is no signal applied to the grid of the second keyer. The SHIFT CPS control sets the grid voltage of reactance control V2, during the FSK mode only, and shifts the output frequency of oscillator V1 by the amount indicated on the SHIFT CPS counter.

In the FAX mode, the TEST switch is turned to LINE and the output of the second keyer is coupled by cathode follower V3B directly across the FUNCTION switch (see dotted line) to the grid of reactance control V2. Note that the SHIFT BALANCE control, SHIFT CALIBRATE control, and SHIFT CPS control are completely bypassed.

In the CW mode, the TEST switch is turned to LINE, and the output of the second keyer is coupled by cathode follower V3B, across the SHIFT BALANCE control and THRESHOLD ADJUSTMENT control R27 to the FUNCTION switch. The THRESHOLD ADJUSTMENT control applies a position of the signal to cathode follower V4A, for coupling to the grid of mixer V5.

4-4. CRYSTAL OSCILLATOR.

Figure 4-4 is a simplified schematic of crystal oscillator V8, temperature controlled ovens Z1 and Z2, and CENTER FREQUENCY switch S6.

The crystal oscillator operates as a conventional circuit with the resonant frequency selected from four plug-in crystals, enclosed in a temperature controlled oven. The proper frequency, selected by the CENTER FREQUENCY switch, is applied to a high gain amplifier (V8) and coupled by a broadband tuned tank (L6) to the grids of mixer V5 and the primary of transformer T4.

Each crystal is enclosed in a temperature controlled oven for optimum stability and is provided with an individual filter capacitor to ensure a clean frequency output. The resonant frequency of each

crystal is selected so that when the output of the crystal oscillator is heterodyned with the frequency shifted output of oscillator V1, the desired audio tone center frequency output selected at the front panel will be developed.

4-5. REACTANCE CONTROLLED OSCILLATOR.

Figure 4-5 is a simplified schematic of oscillator V1 and reactance control V2.

Oscillator V1 is a modified push-pull Colpitts oscillator, generating a 200 kc sine wave output. The major components of the resonant tank circuits are enclosed in a temperature controlled oven with a fast heating characteristic and have extremely uniform retrace characteristics. Trimmer C3 is the coarse adjust control, and trimmer C46 is the fine adjust control. Both these controls are located on the top of the main chassis.

The input signal is applied across the FUNCTION switch to the grids of reactance control V2, and the offset frequency output used to vary the reactive components of the oscillator tank by an amount directly proportional to the applied input. REACTANCE TUBE LINEARITY ADJUSTMENT control C18 sets the reference point for the input voltage so that the frequency will be offset linearly in each direction. The offset frequency output of oscillator V1 is then applied to the grids of mixer V5 (see figure 4-6).

4-6. CW OSCILLATOR AND MIXER.

Figure 4-6 is a schematic diagram of CW oscillator V6A and mixer V5.

In the FAX or FSK mode, the offset frequency output of oscillator V1 (200 kc ± frequency shift) is combined with the reference frequency output of crystal oscillator V8 (198.000 kc + 100 kc, - 500 kc) on the grids of mixer V5, and the difference frequency output (2 kc ± frequency shift) applied across the balanced primary windings of transformer T2 to LEVEL ADJ control R49. The LEVEL ADJ control sets the level of the outputs applied to the load by adjusting the amplitude of the voltage applied to the grids of audio amplifier V7.

MIXER BALANCE ADJUSTMENT control R67 is set at the factory for equal conduction through both sections of mixer V5, and B+ is removed from the CW oscillator when oscillator V1 is conducting.

In the CW mode, only the output of crystal oscillator V8 is applied to the grids of mixer V5, operating as a conventional amplifier, and B+ is applied to the CW oscillator. The 1000 cycle tone generated across the tank circuit coil(L3) and capacitors (C28 and C24) of the CW oscillator is superimposed on the reference frequency from the crystal oscillator and coupled to push-pull audio amplifier V7 across the LEVEL ADJ control. The reference frequency of the crystal oscillator is not passed by transformer T2 because its bandpass is in the audio range.

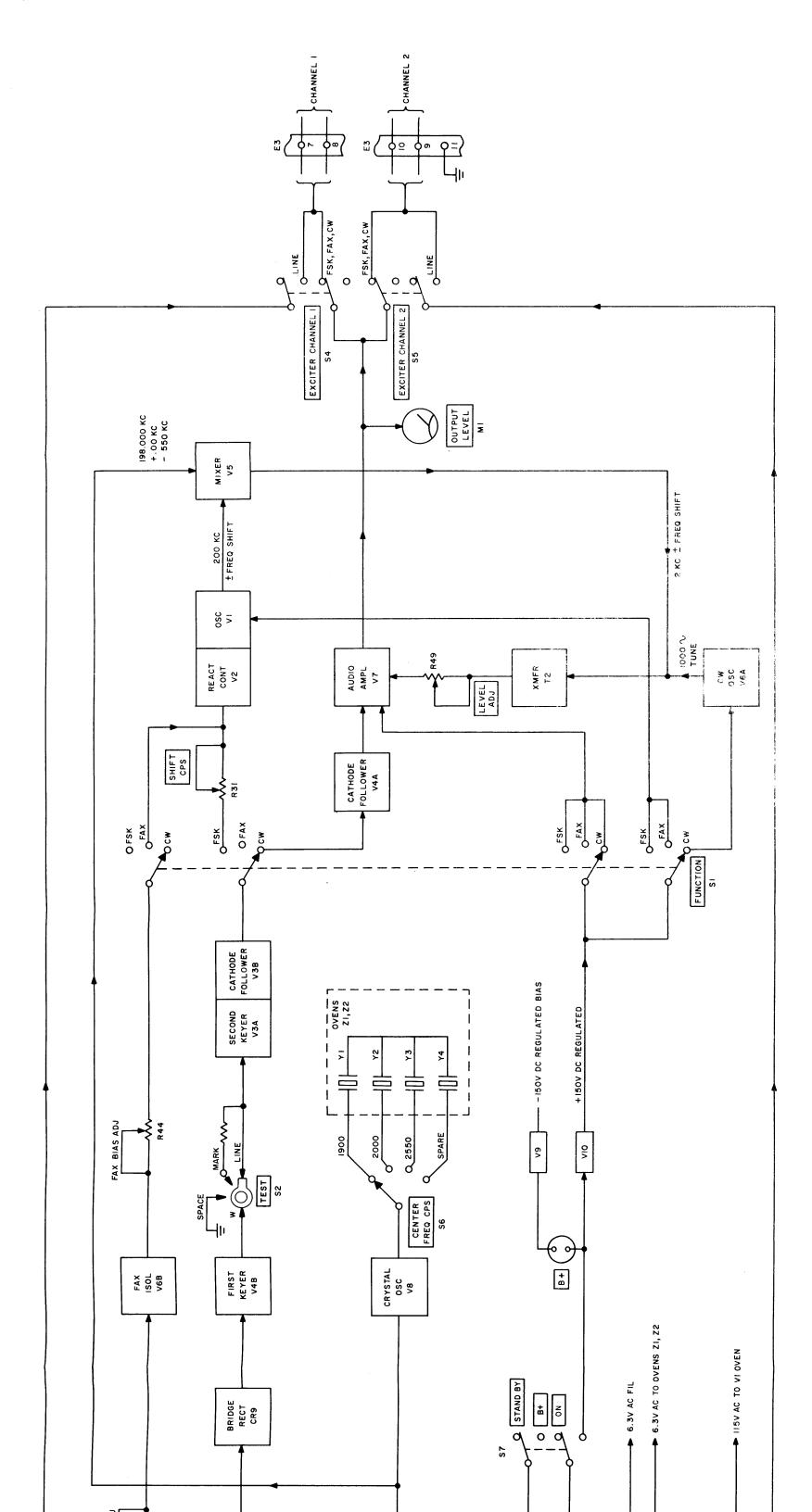
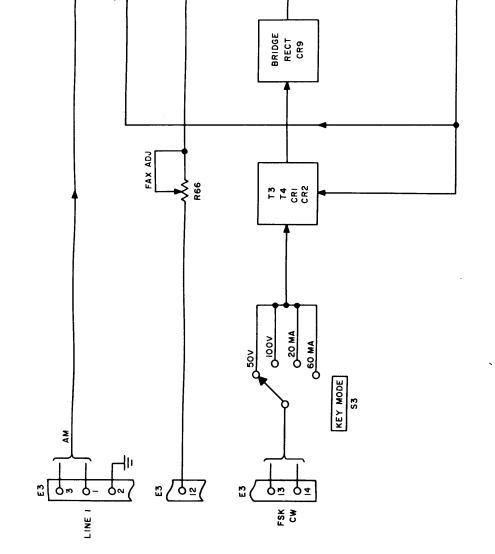
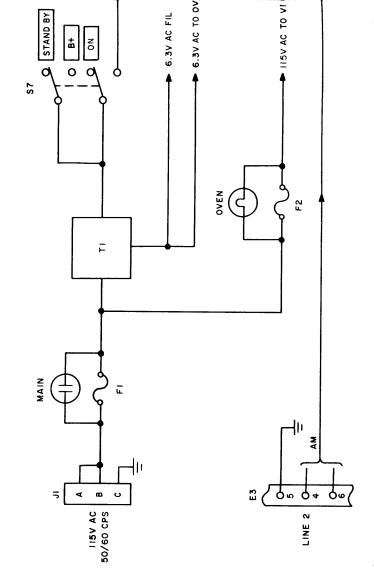


Figure 4-1. Tone Intelligence Unit TIS-3, Overall Block Diagram





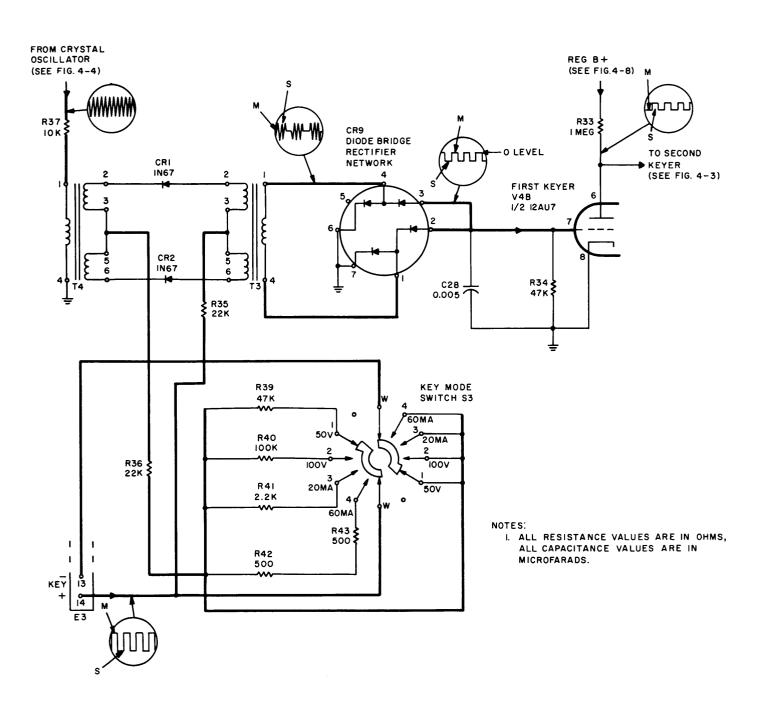


Figure 4-2. First Keyer, Simplified Schematic Diagram

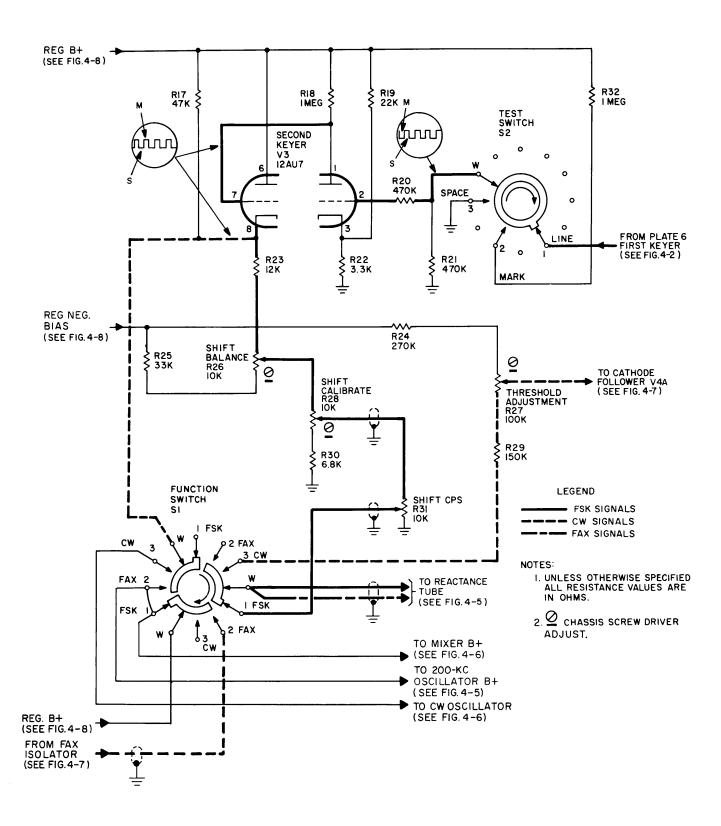
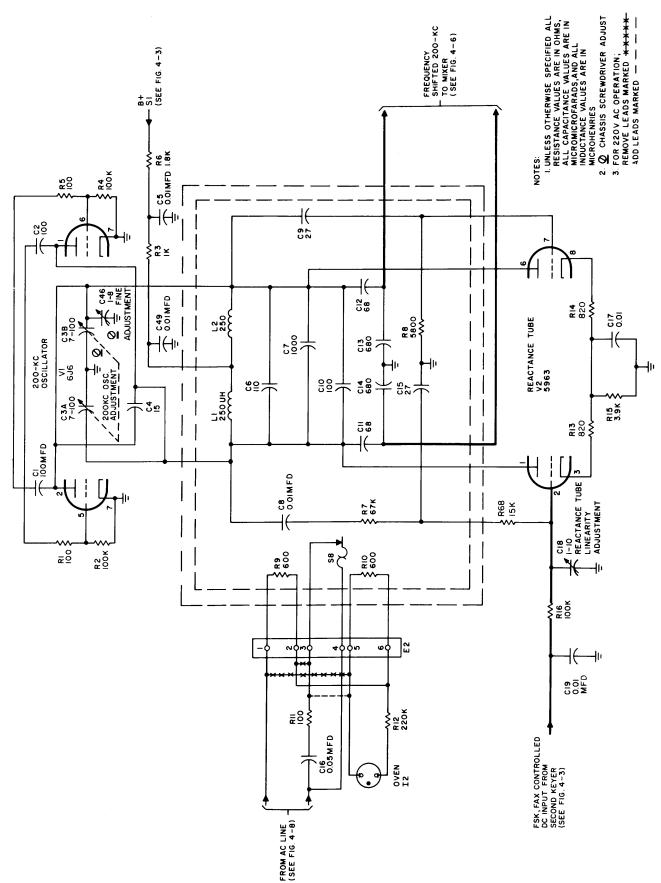


Figure 4-3. Second Keyer, Simplified Schematic Diagram

Figure 4-4. Crystal Oscillator, Simplified Schematic Diagram



200 Kc Oscillator and Reactance Tube, Simplified Schematic Diagram Figure 4-5.

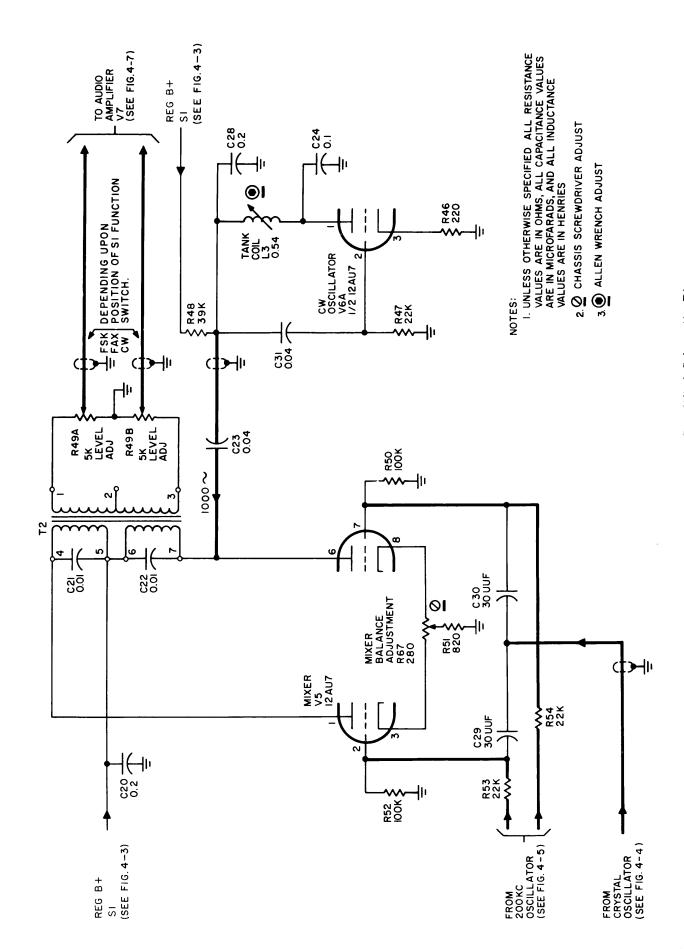


Figure 4-6. CW Oscillator and Mixer, Simplified Schematic Diagram

4-7. FAX ISOLATOR AND AUDIO AMPLIFIER.

Figure 4-7 is a simplified schematic of FAX isolator V6A, cathode follower V4A, and audio amplifier V7.

In the FAX mode, the input pulses are applied across pins 5 and 12 of barrier strip E3, mounted on the rear of the main chassis, and FAX ADJ control R66 to the grid of FAX isolator V6B, operating as a cathode follower. The output of FAX isolator V6B is applied across FAX BIAS ADJUSTMENT control R44 to the grid of reactance control V2 across the FUNCTION switch.

In the FSK and FAX modes, the frequency shifted output of transformer T2 is applied to the grids of audio amplifier V7. The push-pull output of audio amplifier V7 is then applied across transformer T5 to OUTPUT LEVEL meter M1 and EXCITER CH1 switch S4 and EXCITER CH2 switch S5 in parallel.

In the CW mode, the amplified 1000 cycle tone output of transformer T2 is applied to the grids of audio amplifier V7 and B+ is removed from oscillator V1. The amplified CW output pulses from second keyer V3A are coupled across THRESHOLD ADJUSTMENT control R27 and cathode follower V4A to the cathode of audio amplifier V7 across AUDIO BALANCE control R64.

As a result of applying the gated input pulses to the cathode of audio amplifier V7, the push-pull output applied to transformer T5 is gated on and off in synchronization with the input pulses. AUDIO BALANCE control R64 is factory set for equal conduction through both halves of audio amplifier V7.

4-8. POWER SUPPLY.

Figure 4-8 is a simplified schematic of the power supply chassis.

The 115 vac input is applied across fuses F1 and F2 to the primary of power transformers T1 and the heater element of the oven containing the frequency determining elements of oscillator V1.

The secondary of transformer T1 has two filament voltage (6.3 vac) windings and the main winding, connected to the contacts of B+/STANDBY switch S7. One filament winding supplies power to the filaments of the tubes, and the other winding provides power to the heating elements of ovens Z1 and Z2.

When the B+/STANDBY switch is turned ON, approximately 350 vac is applied across a full wave bridge rectifier, consisting of diodes CR3, CR4, CR5, and CR6, and B+ indicator lamp I1 lights.

The -150 volt regulated bias voltage supplied to CW oscillator V6A and cathode followers V3B and V4A is developed from the voltage tapped off the junction of diodes CR4 and CR6 and regulated by voltage regulator V9.

The unregulated 150 vac B+ supply voltage is developed from the voltage tapped off the junction of diodes CR3 and CR5 and applied directly to the stages across a capacitor input filter. The regulated 150 vac B+ is developed from the voltage tapped off the junction of diodes CR3 and CR5 and regulated by voltage regulator V10.

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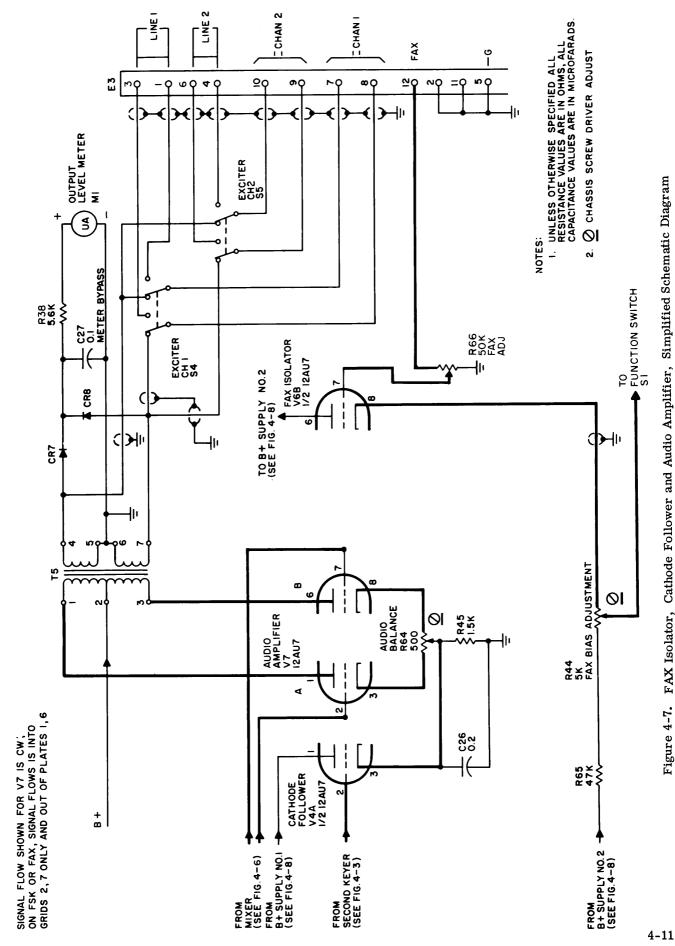
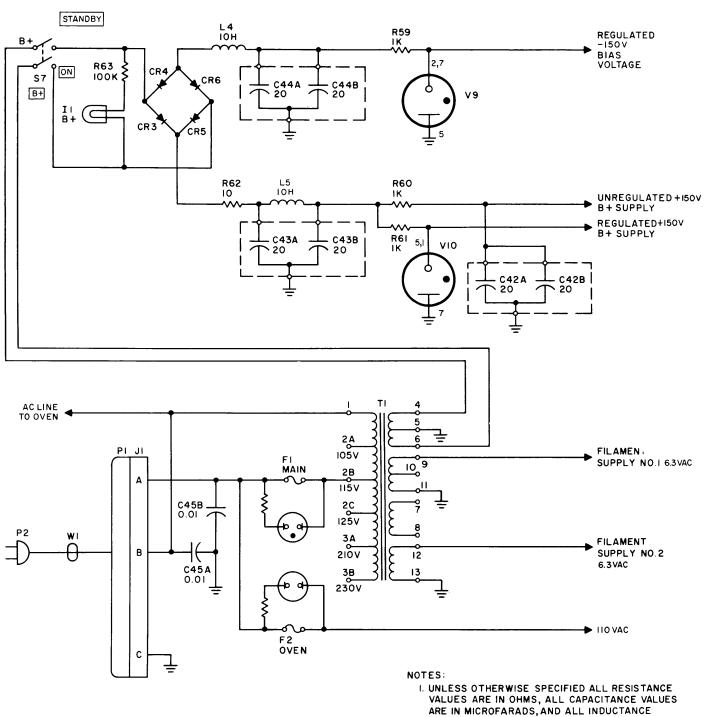


Figure 4-7. FAX Isolator, Cathode Follower and Audio Amplifier, Simplified Schematic Diagram



VALUES ARE IN HENRIES.

Figure 4-8. Power Supply, Simplified Schematic Diagram

4

SECTION 5 TROUBLESHOOTING

5-1. GENERAL.

When a piece of equipment that has been operating satisfactorily suddenly fails, the cause of trouble may be apparent or the symptoms analogous to past failures. If the cause of failure is apparent, a lengthy troubleshooting procedure is not required. Therefore, make certain primary power and an input signal is applied before beginning a lengthy procedure to locate the trouble. In addition, check the fuses and tubes and inspect the underside of the chassis for burned or ruptured components, broken wires and frayed insulation.

Table 5-1 lists a troubleshooting procedure designed to isolate the cause of failure when all conditions seem normal, but the equipment fails to operate (see figure 8-1). Refer to Section 6 of this manual for corrective maintenance procedures after the cause of trouble has been isolated.

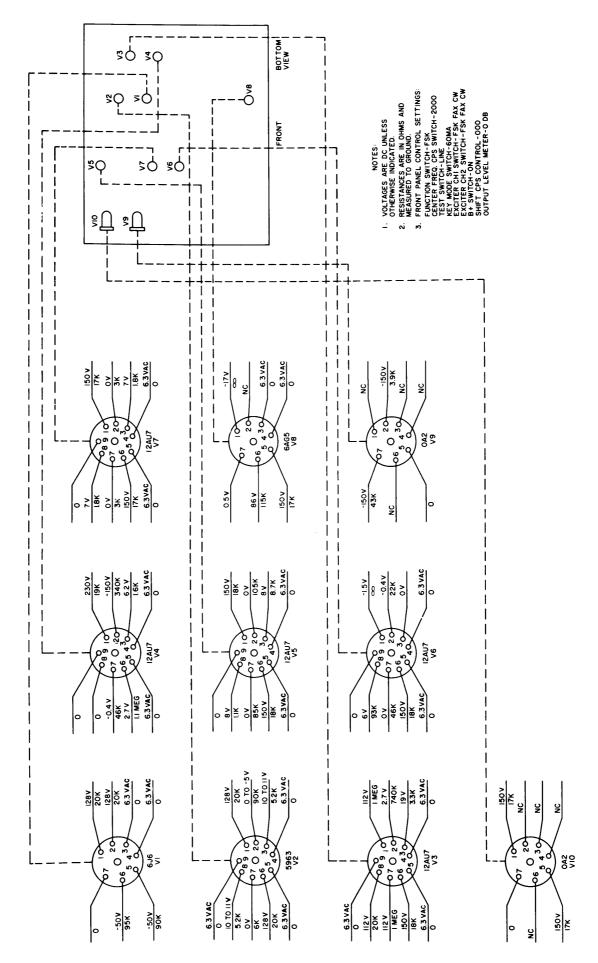
Refer to figure 5-1 for the normal voltage and resistance measurements at the pins of each tube socket. Figures 5-2 through 5-7 illustrate the location of all major components.

TABLE 5-1. TROUBLESHOOTING PROCEDURE

SYMPTOM	PROCEDURE
OVEN indicator lamp does not cycle.	a. Check indicator lamp 12.b. Check 110 vac across pins 1 and 4of terminal strip E2.
In CW mode, OUTPUT LEVEL meter indicates constant level.	 a. Make certain TEST switch is turned to LINE. b. Turn KEY MODE switch to 50 V and apply +50 vdc input across pins 13 and 14 of terminal strip E3. c. Check input voltage on grid of cathode follower V4A. d. Check grid voltage of second keyer. e. Check voltages across secondaries of transformers T3 and T4. f. Remove +50 vdc from pins 13 and 14 of terminal strip E3.
In CW mode, OUTPUT LEVEL meter indicates low level and deflects slightly when handkey is closed.	 a. Make certain TEST switch is turned to LINE. b. Check B+ voltage on CW oscillator. c. Check grid voltage of audio amplifier. d. Check push-pull output of audio amplifier.
In FAX mode, OUTPUT LEVEL meter indicates a constant reading.	 a. Apply +10 vdc input to pin 12 of terminal strip E3. b. Check grid voltage of reactance control tube. c. Check output level of FAX isolator. d. Remove +10 vdc input.
In FAX mode, OUTPUT LEVEL meter indicates zero.	 a. Check -150 vdc bias applied to pin 6 of socket XV1. b. Apply +10 vdc input to pin 12 of terminal strip E3.

TABLE 5-1. TROUBLESHOOTING PROCEDURE (CONT'D)

SYMPTOM	PROCEDURE
	 c. Check inputs and outputs of mixer V5. d. Check input voltage applied to grids of audio amplifier. e. Check output voltage of audio amplifier. f. Remove +10 vdc from pin 12 of terminal strip E3.
In FSK mode, OUTPUT LEVEL meter indicates	a. Check -150 vdc bias applied to pin 2 of socket XV4.
a constant reading.	 b. Turn TEST switch to MARK and check grid voltages of audio amplifier. c. Check output level of cathode follower V3B.
	 d. Turn TEST switch to LINE and turn KEY MODE switch to 50 V. e. Apply +50 vdc across pins 13 and 14 of terminal strip E3.
	f. Check voltages across primaries of transformers T3 and T4. g. Check grid voltage of first keyer.
	h. Remove +50 vdc applied to pins 13 and 14 of terminal strip E3.
In FSK mode, OUTPUT LEVEL meter indicates	a. Check -150 vdc bias applied to pin 6 of socket XV1.
zero.	 b. Turn TEST switch to MARK. c. Check inputs and output of mixer V5. d. Check input voltage to grids of audio amplifier.
	e. Check output level of audio amplifier. f. Turn TEST switch to LINE.



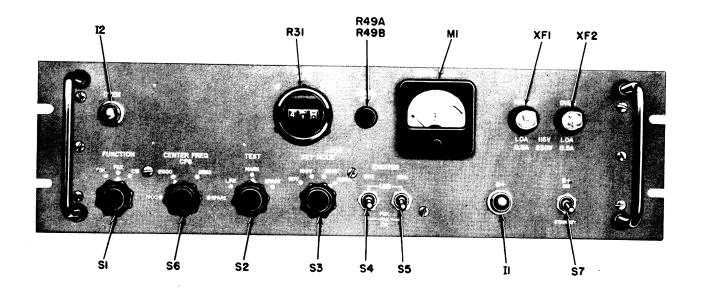


Figure 5-2. Front Panel Operating Controls

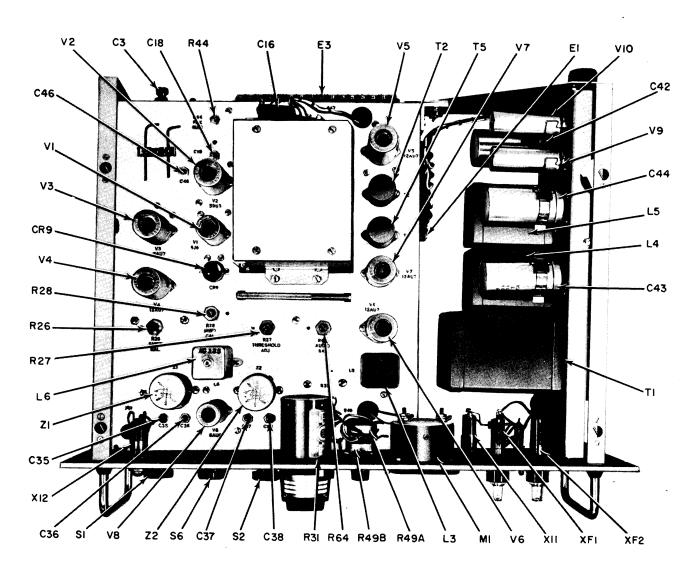


Figure 5-3. Top View of Chassis, Parts Location

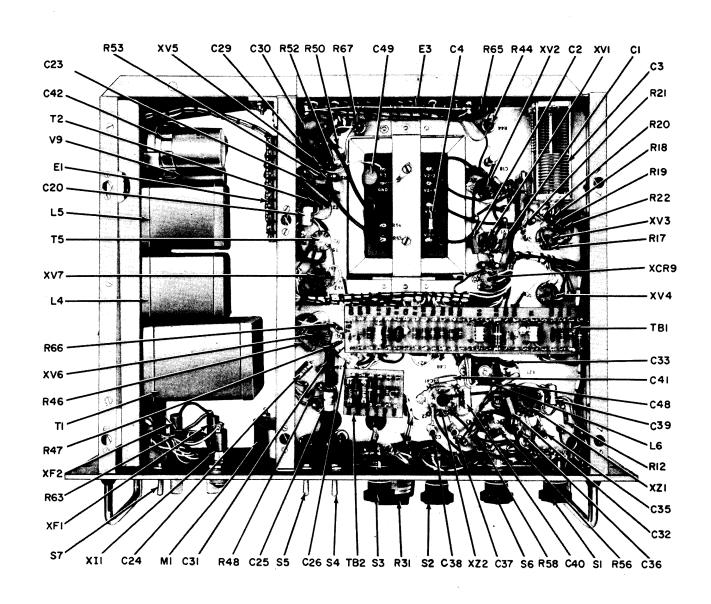


Figure 5-4. Bottom View of Chassis, Parts Location

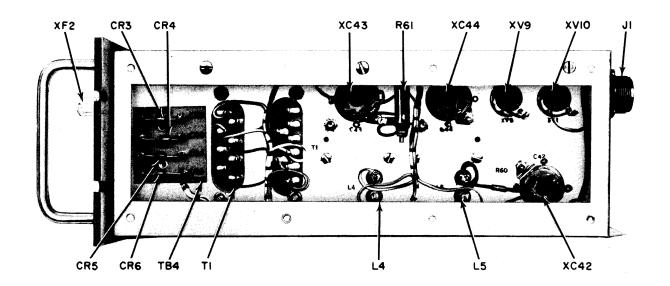


Figure 5-5. Right View of Chassis, Parts Location

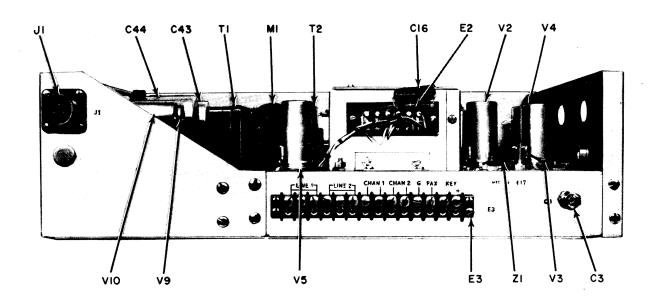
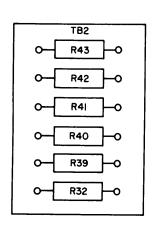


Figure 5-6. Rear View of Chassis, Parts Location



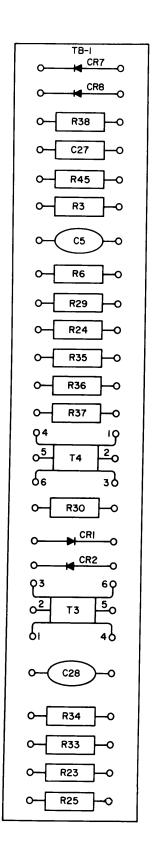


Figure 5-7. Terminal Boards, Parts Location

SECTION 6 MAINTENANCE

6-1. GENERAL.

Periodic maintenance is required to keep the TIS in operating condition and includes cleaning procedures, and instructions for testing the electron tubes. Corrective maintenance contains instructions for tuning and aligning the unit after a faulty component has been located and replaced.

6-2. PREVENTIVE MAINTENANCE.

CLEANING. - The TIS should be cleaned quarterly for optimum reliability. To clean the unit, proceed as follows:

- a. Remove unit from rack and place on workbench.
- b. Remove dust covers and inspect interior of unit for frayed insulation, loose connections, grease, dirt and foreign matter.
- c. Clean chassis with airhose and electronic component with carbon-tetrachloride.

WARNING

When using carbon-tetrachloride, make certain area is well ventilated, and if possible, wear rubber gloves. Avoid inhaling fumes and work only a short period at a time.

TUBE TESTING. - The tubes in this unit should be tested at the same time the cleaning procedure is performed. Remove only one tube at a time to avoid placing a similar tube type in another socket. After each tube has been tested and replaced in socket, make certain it seats properly. When replacing unit in rack, make certain connections to terminal block are tight.

6-3. CORRECTIVE MAINTENANCE.

Table 6-1 lists the alignment procedure. A set of alignment tools are provided with the unit and should be used in making all adjustments. Use the

alignment tool to tune the capacitors and the Allen wrenches for the front panel controls and to tune variable reactor L3. In addition to the alignment tools provided, the following test equipment is required.

- \underline{a} . Hewlett-Packard Voltmeter, Model 410B or equivalent.
- b. Battery supply capable of providing 0 to +20 vdc.
 - c. Berkley Counter, Model 550 or equivalent.
 - d. Simpson Multimeter, Model 260 or equivalent.
- e. Tektronix Oscilloscope, Model 545A or equivalent.
- $\underline{\mathbf{f}}$. Lambda Regulated Power Supply, Model 26 or equivalent.
 - g. C.P. Clare Relay, Model HG-1002 or equivalent.
- h. Measurements Square Wave Generator, Model 71, or equivalent.

To gain access to the main oven components, proceed as follows:

- (1) Disconnect leads to oven terminal board TB2 (see figure 5-4).
- (2) Remove two screws securing terminal board and lift terminal board out of chassis.

NOTE

The heating elements and thermostatic switch will come out with terminal board.

- (3) Disconnect leads to internal components of oven and remove four screws securing oven housing cover.
- (4) Remove oven casting from housing and separate casting from component board.

TABLE 6-1. ALIGNMENT PROCEDURE

STEP	ACTION	NORMAL INDICATION
1	Test power supply. Terminal strip E1.	a. A-c voltage between terminals 1 and 2 of power supply terminal strip is 6.3 V.
		b. A-c voltage between terminals 3 and 4 of power supply terminal strip is 115 v.
		c. D-c voltage to ground at pins 5 and 6 of power supply terminal strip is +150 V.
:		d. D-c voltage to ground at pin 7 of power supply terminal strip is -150 V.
2	Check crystal ovens for proper	Crystal Y1 is 198.100-kc.
	crystals.	Crystal Y2 is 198.000-kc.
		Crystal Y3 is 197.450-kc.
3	Connect oscilloscope and frequency counter through a 50 uuf capacitor to center tap of L6. Place S6 in 2550 position. Tune L6 for maximum voltage on oscilloscope.	Oscilloscope indicates RF voltage of 6 V.
4	Check waveform on oscilloscope for hum.	Hum, should be at least 40 db down.
5	Place S6 in 1900 position. Vary C35.	Frequency counter reads 198.100-kc.
6	Place S6 in 2000 position. Vary C36.	Frequency counter reads 198.000-kc.
7	Place S6 in 2550 position. Vary C37.	Frequency counter reads 197.450-kc.
8	Set C3 to 1/3 maximum. Set C46 to midposition. Set SHIFT CPS control R31 at 000. Set shift calibrate adj. R28 to midposition. Set shift balance adj. R26 to midposition. Set S2 to SPACE. Set S6 to SPARE. Set C18 to 3/4 maximum. Set S1 to FSK. Connect oscilloscope and frequency counter to pin 2 of V5.	200 kc sine wave.
9	Vary C3.	Frequency counter reads 200-kc ± 10 cps. (Lock shaft of C3 when indication is correct.)
10	Vary C46.	Frequency counter reads 200-kc.
11	Read rf voltage with oscilloscope.	Approximately 2 volts. No appreciable distortion or hum is visible.
12	Connect vacuum voltmeter to wiper of shift balance adj. R26. Set meter on 50-volt scale. Adjust meter to center of scale to read ± volts. Place S2 in SPACE position.	Meter reads -12 v. (Vary R26 to secure this reading.)
13	Place S2 in MARK position.	Meter reads +12 V.

TABLE 6-1. ALIGNMENT PROCEDURE (CONT'D)

STEP	ACTION	NORMAL INDICATION
14A	Set SHIFT CPS control R31 to 1000. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of 1000 cps (+500 cps). Vary SHIFT BALANCE ADJ. R26 to secure shift equal on both sides. Vary shift calibrate adj. R28 to secure shift equal to 500 cycles on both sides.
14B	Set R31 to 800. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 802 cps.
14C	Set R31 to 600. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 605 cps.
14D	Set R31 to 500. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 507 cps.
14E	Set R31 to 400. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 407 cps.
14F	Set R31 to 300. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 305 cps.
14G	Set R31 to 200. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 200 cps.
14H	Set R31 to 100. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 102 cps.
14I	Set R31 to 050. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 51 cps.
14J	Set R31 to 000. Change S2 from MARK to SPACE position.	Frequency counter indicates a frequency change of approximately 0 cps.
15	Arrange a test setup as shown in figure 6-1. Place EXCITER CH1 switch S4 in the CW, FAX, FSK position. Place CENTER FREQ CPS control S6 in 1900 position. Set LEVEL ADJ control R49 in midposition. Place S2 in MARK position. Place S1 in FSK position. Set AUDIO BALANCE ADJ. R64 to midposition. , Set R31 to 000.	OUTPUT LEVEL meter reads -20 db. LEVEL ADJ control should adjust level from -20 to 0 db.
16 A	Place S6 in 1900 position.	Frequency counter reads 1900 cps.
16B	Place S6 in 2000 position.	Frequency counter reads 2000 cps.
16C	Place S6 in 2550 position.	Frequency counter reads 2550 cps.
16D	Place S6 in SPARE position.	Frequency counter reads 0 cps.
17	Set LEVEL ADJ control R49 so that OUTPUT LEVEL meter reads 0 db. Check voltage on oscilloscope.	Oscilloscope indicates voltage of 0.78 volt.
18A	Place S6 in 2550 position. Set R31 to 100. Read output frequency with counter.	Frequency counter reads 2500 cps with S2 in SPACE position; 2600 cps with S2 in MARK position.

TABLE 6-1. ALIGNMENT PROCEDURE (CONT'D)

STEP	ACTION	NORMAL INDICATION
18B	Set R31 to 300. Read output frequency with frequency counter.	Frequency counter reads 2400 cps with S2 in SPACE position; 2700 cps with S2 in MARK position.
18C	Set R31 to 500. Read output frequency with frequency counter,	Frequency counter reads 2300 cps with S2 in SPACE position; 2800 cps with S2 in MARK position.
18D	Set R31 to 800. Read output frequency with frequency counter.	Frequency counter reads 2150 cps with S2 in SPACE position; 2950 cps with S2 in MARK position.
18E	Set R31 to 1000. Read output frequency with frequency counter.	Frequency counter reads 2050 cps with S2 in SPACE position; 3050 cps with S2 in SPACE position.
19	Place S1 in FAX position. Arrange a test setup as shown in figure 6-2. Place S6 in 1900 position. Adjust FAX bias adj. R44 for output to the frequency counter of 2300 ± 5 cps when FAX input voltage is 0. Adjust C18 for best linearity. Repeat step 10 after each adjustment of C18.	Proper setting of FAX bias adj. R44.
20A	Set FAX input voltage at 0.5 V.	Frequency counter reads 2260 ± 5% cps.
2 0B	Set FAX input voltage at 1.0 V.	Frequency counter reads 2220 ± 5% cps.
20C	Set FAX input voltage at 2 V.	Frequency counter reads 2140 ± 5% cps.
2 0D	Set FAX input voltage at 5 V.	Frequency counter reads 1900 ± 5% cps
20E	Set FAX input voltage at 6 V.	Frequency counter reads 1820 ± 5% cps.
20F	Set FAX input voltage at 8 V.	Frequency counter reads 1660 ± 5% cps.
2 0G	Set FAX input voltage at 10 V.	Frequency counter reads 1500 ± 5% cps.
21	Arrange a test setup as shown in figure 6-3. Place S1 in CW position. Set threshold adj. R27 to midposition. Place S3 in 60 MA position. Short out contacts of relay and adjust keying current to 60 ma. Set square wave generator to 50 cps. Place S6 in 2000 position. Place S2 in SPACE position.	Proper setting of threshold adj. R27. See also following step 25.
22	Put frequency counter and oscilloscope on pin 2 of V6A. Vary L3 with an Allen wrench.	Frequency counter reads 1000 cps, oscilloscope indicates ac voltage of 25 volts.
23	Short relay and re-read frequency counter and oscilloscope.	Same as step 22.
24	Restore test setup as shown in figure 6-3.	
25	Vary R27 to secure square wave. Check waveshape. Vary R27 so that unkeyed portion is at 0 V.	Vernier adjustment, see step 21.

TABLE 6-1. ALIGNMENT PROCEDURE (CONT'D)

STEP	ACTION	NORMAL INDICATION
26	Disconnect keyline and measure level in unkeyed state.	Tone should be 60 db down.
27A	Vary audio balance adj. R64 for best square wave.	
27B	Check waveform at pin 7 of V4.	
27C	Check waveform at pin 2 of V3.	
27D	Check waveform at pin 8 of V3.	
27E	Check waveform at pin 2 of V4.	
27F	Check waveform at pin 3 of V4.	
28A	Place S3 in 20MA position. Use series potentiometer to obtain levels and repeat steps 27a through 27f.	
28B	Place S3 in 100 V position. Use series potentiometer to obtain levels and repeat steps 27a through 27f.	
28C	Place S3 in 50 V position. Use series potentiometer to obtain levels and repeat steps 27a through 27f.	
29	Set S1 to FSK. Set shift to 1000 cps. Check shifted tone for audio distortion. Check from 0 to 1000 cps shift.	Top should be flat ± 1 db.
30	Connect panoramic analyzer to terminals 7 and 8 at rear of chassis. Place S1 in FSK position. Set R31 to 000. Place S6 in 2000 position. Place S2 in LINE position. Set OUTPUT LEVEL meter to 9 db. Vary R67.	Minimum distortion and hum 40 db down as viewed on the panoramic analyzer.

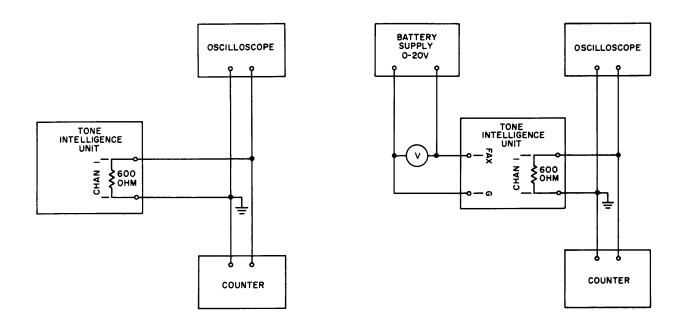


Figure 6-1. Test Setup No. 1

Figure 6-2. Test Setup No. 2

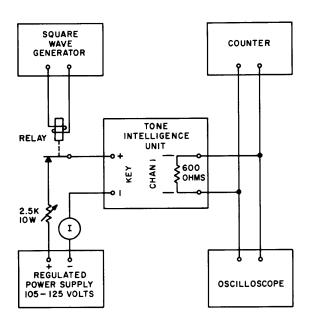


Figure 6-3. Test Setup No. 3

SECTION 7 PARTS LIST

7-1. GENERAL.

Reference designations have been assigned to identify all maintenance parts of the equipment. They are used for marking the equipment (adjacent to the part they identify) and are included on drawings, diagrams and the parts list. The letters of a reference designation indicate the kind of part (generic group), such as resistor, capacitor, electron tube, etc. The number differentiates between parts of the same generic group. Sockets associated with a particular plug-in device, such as electron tube or

fuse, are identified with a reference designation which includes the reference designation of the plugin device. For example, the socket for fuse F1 is designated XF1. Column 1 lists the reference designations of the various parts in alphabetical and numerical order. Column 2 lists and describes the various parts. Major part assemblies are listed in their entirety; subparts of a major assembly are listed in alphabetical and numerical order with reference to the major assembly. Column 3 indicates the function of each part within a major component. Column 4 lists each Technical Materiel Corporation part number.

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
C1	CAPACITOR, fixed: mica; 100 uuf, $\pm 10\%$, char. C, 500 wvdc.	Coupling	CM20C101J
C2	Same as C1	Coupling	
C3	CAPACITOR, variable: air dielectric; dual section, 7 to 100 uuf each section.	Tuning	CB-103-3
C4	CAPACITOR, fixed: ceramic; 15 uuf, ± 0.5 uuf, char. N750, 600 wvdc.	Padder	CC-103-1
C5	CAPACITOR, fixed: ceramic disc; 0.01 mfd, 500 wvdc.	Plate Bypass	CC-100-16
C6	CAPACITOR, fixed: ceramic; 110 uuf, ±2%, 500 wvdc.	Temperature Compensation	CC26UJ111G
C7	CAPACITOR, fixed: ceramic; 1000 uuf, $\pm 1\%$, 500 wvdc.	Tank	CC-102-4
C8	CAPACITOR, fixed: mica; 100 uuf, ±10%, 500 wvdc.	Phase Shift	CM20B102K
С9	CAPACITOR, fixed: mica; 27 uuf, ±2%, 500 wvdc.	Phase Shift	CM20E270G
C10	Same as C6		
C11	CAPACITOR, fixed: ceramic; 68 uuf, ±5%, 500 wvdc.	Coupling	CC-102-1
C12	Same as C11		
C13	CAPACITOR, fixed: mica; 680 uuf, $\pm 2\%$, char. E, 500 wvdc.	Divider	CM20E681G
C14	Same as C13	Divider	
C15	Same as C9		
C16	CAPACITOR, fixed: mylar dielectric; 0.05 mfd, 5%, 400 wvdc.	Arc Suppressor	CN-113-1

DEE		 	TMC
REF SYM	DESCRIPTION	FUNCTION	PART NO.
C17	Same as C5	Cathode Bypass	
C18	CAPACITOR, variable: glass; 1.0-12.0 uuf, operating temp55°C to 125°C, 500 wvdc.	Linearity Ad- justment	CV-101-3
C19	Same as C5		
C20	CAPACITOR, fixed: mylar; 0.2 mfd, ±10%, 200 wvdc.	Decoupling	CN108C2003K
C21	Same as C5	RF Bypass	
C22	Same as C5	RF Bypass	
C23	CAPACITOR, fixed: metallized paper; 0.04 mfd, $\pm 10\%$, 400 wvdc.	Dc Blocking	CP106C403-4
C24	CAPACITOR, fixed: mylar; 0.1 mfd, ±10%, 200 wvdc.		CN106C104K
C25	Same as C20		
C26	Same as C20	Cathode Bypass	
C27	Same as C24	Meter Bypass	
C28	CAPACITOR, fixed: ceramic disc; 0.005 mfd, 500 wvdc.	Grid Leak Bias	CC-100-15
C29	CAPACITOR, fixed: mica; 3 uuf, $\pm 5\%$, char. C, 500 wvdc.	Coupling	См20С300Ј
C30	Same as C29		
C31	Same as C23		
C32	CAPACITOR, fixed: mica; 270 uuf, $\pm 2\%$, char. C, 500 wvdc.	Coupling	CM20C271G
C33	Same as C5	Decoupling	
C34	Same as C5	RF Bypass	
C35	CAPACITOR, variable: glass; 1 to 8 uuf, 1000 vdc at mid-capacity.	Trimmer	CV-104-1
C36	CAPACITOR, variable: glass; 1 to 8 uuf, 1000 vdc at mid-capacity.	Trimmer	CV-101-1
C37	Same as C36		
C38	Same as C36		
C39	Same as C24	Stabilizing	
C40	Same as C24	Stabilizing	
C41	CAPACITOR, fixed: mica; 27 uuf, $\pm 5\%$, char. C, 500 wvdc.	Bypass	CM20C270J
C42	CAPACITOR, fixed: dry electrolytic; polarized; dual unit; 20 mfd, each section, char. E, 450 wvdc.	Filter	CE52E200R

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
C43	Same as C42	Filter	
C44	Same as C42	Filter	
C45	CAPACITOR, fixed: ceramic disc; two section; 0.01 mfd, 500 wvdc.	Bypass	CC-100-23
C46	Same as C36	Fine Adjust	
C47	CAPACITOR, fixed: ceramic; 8 uuf, ± 0.25 uuf, 500 wvdc.	RF Bypass	CC21SL080C
C48	CAPACITOR, fixed: mica; 220 uuf, $\pm 2\%$, char. C, 500 wvdc.	RF Bypass	CM20C221G
C49	Same as C5	RF Bypass	
C50	CAPACITOR, fixed: paper; 5 mfd, ±10%, 600 wvdc.	Filter	CN112A504K6
C51	CAPACITOR, fixed: mica; 50 uuf, ±2%.	RF Bypass	CM20C0505
CR1	DIODE, germanium.	Rectifier	1N67
CR2	Same as CR1	Rectifier	
CR3	DIODE, silicon, rectifier: max. 210 volts rms, max. 600 peak inverse volts.	Rectifier	1N-547
CR4	Same as CR3		
CR5	Same as CR3		
CR6	Same as CR3		
CR7	Same as CR1		
CR8	Same as CR1		
CR9	DIODE ASSY, germanium.	Bridge Rectifier	DD-100
F1	FUSE, cartridge: time lag; 1 amp.	Main Power	FU-102-1
F2	Same as F1	Oven	
I1	LAMP, neon: miniature; 100-125 volts, 1/25 watt, T-3-1/4 bulb; bayonet base.	B+	ВІ-100-51Н
12	LAMP, neon: miniature; 110 volts, 1/25 watt, T-3-1/4 bulb; bayonet base.	Oven	BI-100-51
J1	CONNECTOR, receptacle: male; 3 contacts.	Power Input	MS3102A-16S-5P
L1	COIL, R.F.: fixed; 250 uhy, ± 1 uhy, Q=100, test frequency, 790 kc.	Tank	CL-265
7 0			

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
L2	Same as L1		
L3	REACTOR, variable: 0.54-1 hy, 15 ma dc max.	Tank Coil	CL-261
L4	REACTOR, filter: 10 hy, 70 ma; 280 ohms dc resistance.	Filter Choke	TF-5006
L 5	Same as L4		
L6	COIL, R.F.: tuned; 1.5-3.5 mhy; Q=50 at 250 kc.	Tank	AC-135
L7	COIL, R.F.: 2.5 mhy; 100 ma.	RF Choke	CL-140-1
М1	METER, level indicating.	Output	MR-144
P1	CONNECTOR, receptacle: female; 3 contacts.	Power Plug	MS3106A-16S-5S
P2	PLUG, connector: male; 2 prong.	Power Plug	PL-171
R1	RESISTOR, fixed: composition; 100 ohms, $\pm 10\%$, $1/2$ watt.	Parasitic Suppressor	RC20GF101K
R2	RESISTOR, fixed: composition; 100,000 ohms, $\pm 10\%$, $1/2$ watt.	Grid Leak	RC20GF104K
R3	RESISTOR, fixed: composition; 1000 ohms, $\pm 10\%$, $1/2$ watt.	Plate Load	RC20GF102K
R4	Same as R2		
R5	Same as R1		
R6	RESISTOR, fixed: composition; 1800 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF182K
R7	RESISTOR, fixed: metallized film; 67,000 ohms, $\pm 1\%$, $1/2$ watt.	Phase Shift	RR-104-673
R8	RESISTOR, fixed: metallized film; 5800 ohms, $\pm 1\%$, $1/2$ watt.	Phase Shift	RR-104-582
R9	RESISTOR, fixed: wirewound; cartridge heater type, 20 watts at 115 volts ac $\pm 10\%$, 1000 v insulated to shell.	Over Heater	RR-102-1
R10	Same as R9		
R11	Same as R1	Arc Supressor	
R12	RESISTOR, fixed: composition; 220,000 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF224K
R13	RESISTOR, fixed: composition; 820 ohms, $\pm 10\%$, $1/2$ watt.	Cathode	
R14	Same as R13		

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
R15	RESISTOR, fixed: composition; 3900 ohms, $\pm 10\%$, $1/2$ watt.	Cathode	RC20GF392K
R16	Same as R2		
R17	RESISTOR, fixed: composition; 47,000 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF473K
R18	RESISTOR, fixed: composition; 1 megohm, $\pm 10\%$, $1/2$ watt.	Plate Load	RC20GF105K
R19	RESISTOR, fixed: composition; 22,000 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF223K
R20	RESISTOR, fixed: composition; 470,000 ohms, $\pm 10\%$, $1/2$ watt.	Grid Limiter	RC20GF474K
R21	Same as R20	Grid Leak	
R22	RESISTOR, fixed: composition; 3300 ohms, $\pm 10\%$, $1/2$ watt.	Cathode Bias	RC20GF332K
R23	RESISTOR, fixed: composition; 12,000 ohms, $\pm 10\%$, 1 watt.	Cathode Bias	RC32GF123K
R24	RESISTOR, fixed: composition; 270,000 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF274K
R25	RESISTOR, fixed: composition; 33,000 ohms, $\pm 10\%$, 2 watts.	Voltage Dropping	RC20FG333K
R26	RESISTOR, variable: composition; 10,000 ohms, $\pm 10\%$, 2 watts, w/locking bushing.	Shift Balance	RV4ATXA103A
R27	RESISTOR, variable: composition; 100,000 ohms, $\pm 10\%$, 2 watts.	Threshold Adjustment	RV4ATXA104A
R28	Same as R26	Shift Calibration	
R29	RESISTOR, fixed: composition; 150,000 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF154K
R30	RESISTOR, fixed: composition; 6800 ohms, $\pm 10\%$, 2 watts.	Voltage Dropping	RC42GF682K
R31	RESISTOR, variable: precision; 10,000 ohms, 0.1% - 0.05% , 5 watts.	Shift Control	RP-102-103
R32	Same as R18	Voltage Dropping	
R33	Same as R18		
R34	Same as R17	Grid Detector	
R35	Same as R19	Equalizing	
R36	Same as R19	Equalizing	

R37	DEGREEO C. 1		PART NO.
	RESISTOR, fixed: composition; 10,000 ohms, $\pm 10\%$, 1/2 watt.	Voltage Dropping	RC20GF103K
R38	RESISTOR, fixed: composition; 5600 ohms, $\pm 10\%$, $1/2$ watt.	Meter Load	RC20GF562K
R39	Same as R17		
R40	Same as R2	Voltage Dropping	
R41	RESISTOR, fixed: composition; 2200 ohms, $\pm 10\%$, 2 watts.	Current Limiter	RC42GF222K
R42	RESISTOR, fixed: wirewound; 500 ohms, 100 ma dc, 5 watts.	Current Limiter	RW-107-28
R43	Same as R42		
R44	RESISTOR, variable: composition; 5000 ohms, $\pm 10\%$, $1/2$ watt.	Bias	RV106UX8B502A
R45	RESISTOR, fixed: composition; 1500 ohms, $\pm 10\%$, $1/2$ watt.	Audio Balance	RC20GF152K
R46	RESISTOR, fixed: composition; 220 ohms, $\pm 10\%$, $1/2$ watt.	Cathode Bias	RC20GF221K
R47	Same as R19	Grid Detector	
R48	RESISTOR, fixed: composition; 39,000 ohms, $\pm 10\%$, $1/2$ watt.	Voltage Dropping	RC20GF393K
R49	RESISTOR, variable: composition; dual unit, 5000 ohms, $\pm 10\%$, 2 watts, w/locking bushing.	Level Adjust	RV104ATSA502C
R50	Same as R2	Grid Leak	
R51	RESISTOR, fixed: composition; 820 ohms, $\pm 10\%$, $1/2$ watt.	Cathode Bias	RC20GF281K
R52	Same as R2		
R53	Same as R19	Grid Limiter	
R54	Same as R19	Grid Limiter	
R56	Same as R20	Grid Detector	
R57	Same as R46		
R58	Same as R2	Voltage Dropping	
R59	RESISTOR, fixed: composition; 1000 ohms, $\pm 10\%$, $1/2$ watt.	Plate Load	RC42GF102K
R60	Same as R3	Voltage Dropping	

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
R61	RESISTOR, fixed: wirewound; 1000 ohms, $\pm 5\%$, 10 watts.	Plate Load	RW-109-24
R62	RESISTOR, fixed: composition; 10 ohms, $\pm 10\%$, $1/2$ watt.	Load	RC20GF100K
R63	RESISTOR, fixed: composition; 100,000 ohms, $\pm 10\%$, 2 watts.	Voltage Dropping	RC42GF104K
R64	RESISTOR, variable: composition; 500 ohms, $\pm 10\%$, 2 watts.	Cathode	RV4ATXA501A
R65	RESISTOR, fixed: composition; 47,000 ohms, $\pm 10\%$, 1 watt.	Bias	RC32GF473K
R66	RESISTOR, variable: composition; 50 k, $\pm 10\%$, $1/2$ watt.	Fax. Adj.	RV106UX8B503A
R67	RESISTOR, variable: composition; 250 Ω , ±10 $\%$, 1/2 watt.	Mixer Balance	RV106UX8B251A
R68	RESISTOR, fixed: composition; 15 k, $\pm 10\%$, $1/2$ watt.	Shift Bias	RC20GF153K
S1	SWITCH, rotary: 1 section; 3 positions; bakelite insulation; contacts and wipers silver plated; $1/4$ in. drive shaft, $7/8$ in. lg, flatted $3/8$ in.	FUNCTION Switch	SW-148
S2	SWITCH, rotary: non-shorting; single section, one pole, 3 positions, $1-5/16$ in. wafers.	TEST Switch	SW-111
S3	SWITCH, rotary: 4 positions; 2 poles, 1 section, non-shorting contacts.	KEY MODE Switch	SW-120
S4	SWITCH, toggle: DPDT; 3 amp, 250 v.	EXCITER CH1 Switch	ST-22N
S5	Same as S4	EXCITER CH2 Switch	
S6	SWITCH, rotary: 1 section; 4 positions, angle of throw 60 $^{\circ}$.	CENTER FREQ Switch	SW-281
S7	SWITCH, toggle: DPDT; 6 amps.	B+ ON/STAND- BY Switch	ST-22K
S8	SWITCH, thermostatic: operate at 70°C, ±2°C.		SS-100-2
Т1	TRANSFORMER, power: hermetically sealed; insulation -all winding insulated for 1000 volts; primary winding taps - 105V, 115V, 125V, 210V, 230V, 50/60 cps, 1 phase; secondary winding taps #1 175-0-175 V RMS, 100 ma dc operating into 35 uf capacitor input; #2 6.3 V at 3.6 amp CT; #3 - 6.3 V at 1.0 amp; #4 6.3 V at 2.5 amp.	Power Trans- former	TF-227
Т2	TRANSFORMER, audio: primary 20,000 ohms CT; secondary 150, 600 ohms; 4 ma dc in primary; ±2 db, 200 to 10,000 cps.	Coupling Transformer	TF-138

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
Т3	TRANSFORMER, pulse: 3 windings; 10 uhy, $\pm 20\%$, 500 wvdc.	Coupling Transformer	TF-228B5
Т4	Same as T3	Coupling Transformer	
Т5	Same as T2		
V1	TUBE, electron: duo-triode; 7 pin miniature.	200 Kc Oscil- lator	6J6
V2	TUBE, electron: dual troide; 9 pin miniature.	Reactance	5963
V3	TUBE, electron: medium-mu duo-triode; 9 pin miniature.	2nd Keyer	12AU7
V4	Same as V3	1st Keyer	
V5	Same as V3	Mixer Tube	
V6	Same as V3	CW Oscillator/ FAS Isolator	
V7	Same as V3	Audio Amplifier	
v8	TUBE, electron: sharp cutoff RF pentode; 7 pin miniature.	Xtal Oscillator	6AG5
V9	TUBE, electron: voltage regulator; 7 pin miniature.	Negative Bias	0A2
V10	Same as V9	Voltage Regulator	
W1	CABLE ASSY., power: coiled 2 conductor, retractible to 1" - 0".	Input Power	CA-581
XC42	SOCKET, electron tube: octal.	C42	TS101P01
XC43	Same as XC42	C43	
XC44	Same as XC42	C44	
XCR9	SOCKET, electron tube: 7 pin miniature; bottom mounting, bakelite, mica filled insulation, with center shield.	CR9	TS-130-MPW
XF1	FUSEHOLDER, lamp: indicating; 100 to 250 v, 20 amps, consisting of neon lamp w/clear knob and resistor 220,000 ohms.	MAIN Fuse F1	FH-104-3
XF2	Same as XF1	OVEN Fuse F2	
XI1	LIGHT, indicator: w/red frosted lens; for miniature bayonet base T-3-1/4 bulb.	11	TS-106-1

REF SYM	DESCRIPTION	FUNCTION	TMC PART NO.
XI2	LIGHT, indicator: w/clear white lens; for miniature bayonet base T-3-1/4 bulb.	12	TS-106-2
XV1	SOCKET, electron tube: 7 pin miniature.	V1	TS102P01
XV2	SOCKET, electron tube: 9 pin miniature.	V2	TS103P01
XV3	Same as XV2	V3	
XV4	Same as XV2	V4	
XV5	Same as XV2	V5	
XV6	Same as XV2	V6	
XV7	Same as XV2	V7	
XV8	Same as XV1	V8	
XV9	Same as XV1	v9	
XV10	Same as XV1	V10	
XZ1	Same as XC42	Z1	
XZ2	Same as XC42	Z2	
Y1	CRYSTAL, quartz: 198.100 kc.	Oscillator	CR-47/U-0.1981 00-P
Y2	CRYSTAL, quartz: 198.000 kc.	Oscillator	CR-47/U-0/1980 00-P
Y3	CRYSTAL, quartz: 197.450 kc.	Oscillator	CR-47/U-0.1974 50-P
Z1	OVEN, crystal: 6.3 v, 7.5 watt heater; 75° C $\pm 2^{\circ}$ C; octal base.	Xtal Oven	PO-158-1
Z2	Same as Z1	Xtal Oven	

SECTION 8 SCHEMATIC DIAGRAMS

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Figure 8-1. Tone Intelligence Unit TIS-3, Detailed Schematic Diagram

